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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
10/087,706	02/28/2002	Yuqun Zeng	12113/46002	12113/46002 7984	
26646 75	11/23/2005		EXAM	EXAMINER	
KENYON & KENYON			PATEL, ISHWARBHAI B		
ONE BROADWAY NEW YORK, NY 10004			ART UNIT	PAPER NUMBER	
			2841		
			DATE MAILED: 11/23/2005	DATE MAILED: 11/23/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

Advisory Action Before the Filing of an Appeal Brief

Application No.	Applicant(s)	
10/087,706	ZENG, YUQUN	
Examiner	Art Unit	_
Ishwar (I. B.) Patel	2841	

Before the Filling of all Appear Brief	Examiner	Art Unit						
	Ishwar (I. B.) Patel	2841						
The MAILING DATE of this communication appe	ars on the cover sheet with the d	correspondence add	ress					
THE REPLY FILED 07 November 2005 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.								
The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:								
a) \square The period for reply expires 3 months from the mailing date of	The period for reply expires 3 months from the mailing date of the final rejection.							
b) The period for reply expires on: (1) the mailing date of this Adverent, however, will the statutory period for reply expire later the Examiner Note: If box 1 is checked, check either box (a) or (b). MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f)	an SIX MONTHS from the mailing date of ONLY CHECK BOX (b) WHEN THE FI	f the final rejection.						
Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b). NOTICE OF APPEAL								
2. The Notice of Appeal was filed on A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).								
AMENDMENTS								
3. The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will <u>not</u> be entered because (a) They raise new issues that would require further consideration and/or search (see NOTE below);								
 (b) ☐ They raise the issue of new matter (see NOTE below) (c) ☐ They are not deemed to place the application in began appeal; and/or 		educing or simplifying	the issues for					
(d) They present additional claims without canceling a	corresponding number of finally re	iected claims.						
NOTE: (See 37 CFR 1.116 and 41.33(a)).		,						
4. The amendments are not in compliance with 37 CFR 1.1		ompliant Amendment	(PTOL-324).					
5. Applicant's reply has overcome the following rejection(s		•	,					
 Newly proposed or amended claim(s) would be a the non-allowable claim(s). 	llowable if submitted in a separate	, timely filed amendm	ent canceling					
7. For purposes of appeal, the proposed amendment(s): a) how the new or amended claims would be rejected is pro	•	rill be entered and an	explanation of					
The status of the claim(s) is (or will be) as follows: Claim(s) allowed:								
Claim(s) allowed: Claim(s) objected to:								
Claim(s) rejected: <u>1-4 andf 6-14</u> .								
Claim(s) withdrawn from consideration:								
AFFIDAVIT OR OTHER EVIDENCE								
8. The affidavit or other evidence filed after a final action, be because applicant failed to provide a showing of good an and was not earlier presented. See 37 CFR 1.116(e).	▼							
 The affidavit or other evidence filed after the date of filing entered because the affidavit or other evidence failed to d 	overcome <u>all</u> rejections under appe	al and/or appellant fa	ils to provide a					
showing a good and sufficient reasons why it is necessar 10. The affidavit or other evidence is entered. An explanation	•	` ''	•					
REQUEST FOR RECONSIDERATION/OTHER	on or the status of the claims after t	entry is below or attac	ned.					
11. The request for reconsideration has been considered but does NOT place the application in condition for allowance because:								
See Continuation Sheet.								
12. 🔲 Note the attached Information Disclosure Statement(s). (PTO/SB/08 or PTO-1449) Paper No(s) 13. 🔲 Other:								
13. [_] Other		Palvi	A4					
			h - '					

Continuation of 11. does NOT place the application in condition for allowance because: Applicant's argument that the secondary prior art of Annis or Takami does not disclose the deficiency of the primary art of Kawakami is not found persuasive. In the absence of the specific structure of connecting cables either in the detail description or in the figures, the examiner interpreted the claimed limitation "wherein the said dissipative coating layer is applied on all connecting cables of ESD sensitive devices" broadly, which a person of ordinary skill in the art would have interpreted and the prior art of Annis and Takami were used to show the cable connection of the component. Those prior arts were not used to show the dissipative coating layer. If the component is to be protected from the static charge than all the connection to the component also has to be provided with same coating. As an additional support, Frederickson et al., US Patent No. 5,160,374, in figure 1 and 2, discloses a semi-insulating layer (paint, 24) for surface discharge problem and further states that the layer (paint) must cover all the surfaces including integrated circuit packages. Also, it can be seen in figure 2, the wire, where it is connected to the board is also covered with the coating, column 4, line 30-40. Therefore, it would have been obvious to a person of ordinary skill in the art at the time of applicant's invention to provide the structure of Kawakami with coating layer applied on all connecting cables of ESD sensitive device, in order to protect the device from the damage caused by generation of static charge.

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